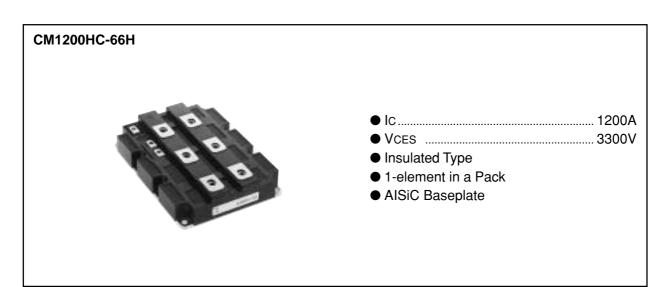
MITSUBISHI HVIGBT MODULES

CM1200HC-66H

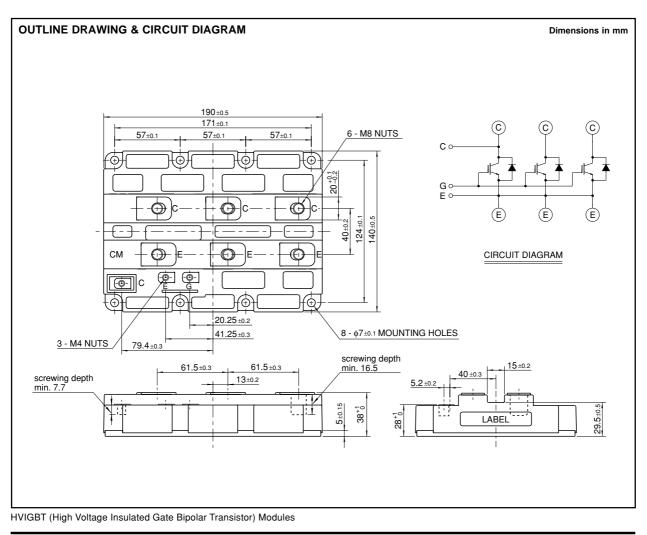
 HIGH POWER SWITCHING USE

 3rd-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules
 INSULATED TYPE



APPLICATION

Traction drives, High Reliability Converters / Inverters, DC choppers





HIGH POWER SWITCHING USE

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INSULATED TYPE

MAXIMUM RATINGS

Symbol	Item	Conditions		Ratings	Unit
VCES	Collector-emitter voltage	VGE = 0V, Tj = 25°C		3300	V
Vges	Gate-emitter voltage	VCE = 0V, Tj = 25°C		±20	V
Ic	Collector current	Tc = 100°C		1200	Α
Ісм	Collector current	Pulse	(Note 1)	2400	Α
IE (Note 2)	Emitter current			1200	Α
IEM(Note 2)	Emiller current	Pulse	(Note 1)	2400	A
PC (Note 3)	Maximum power dissipation	Tc = 25°C, IGBT part		14700	W
Tj	Junction temperature			-40 ~ +150	°C
Тор	Operating temperature			-40 ~ +125	°C
Tstg	Storage temperature			-40 ~ +125	°C
Viso	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1min.		6000	V
tpsc	Maximum short circuit pulse width	$\label{eq:VCC} \begin{array}{l} \mbox{VCC} = 2200\mbox{V}, \mbox{VCES} \leq 3300\mbox{V}, \mbox{VGE} = 15\mbox{V}\\ \mbox{T}_{j} = 125^{\circ}\mbox{C} \end{array}$		10	μs

ELECTRICAL CHARACTERISTICS

Symbol	Item	O an ditiana		Limits		
		Conditions		Тур	Max	Unit
ICES	Collector cut-off current	VCE = VCES, VGE = 0V, Tj = 25° C	_	—	15	mA
VGE(th)	Gate-emitter threshold voltage	IC = 120mA, VCE = 10V, Tj = 25°C	5.0	6.0	7.0	~
IGES	Gate leakage current	VGE = VGES, VCE = 0V, Tj = 25°C	_	_	0.5	μA
	Collector-emitter	IC = 1200A, VGE = 15V, Tj = 25°C (Not	e 4) —	3.30	4.20	20 V
VCE(sat)	saturation voltage	IC = 1200A, VGE = 15V, Tj = 125°C (Not	e 4) —	3.60	_	
Cies	Input capacitance	VCE = 10V, f = 100kHz		180	—	nF
Coes	Output capacitance	$V_{GE} = 0V, T_i = 25^{\circ}C$		18.0		nF
Cres	Reverse transfer capacitance	VGE = 0V, 1j = 20 0		5.4		nF
Qg	Total gate charge	VCC = 1650V, IC = 1200A, VGE = 15V, Tj = 25°C	_	8.6	—	μC
VEC(Note 2)	Emitter-collector voltage	$IE = 1200A, VGE = 0V, Tj = 25^{\circ}C$ (Not	e 4) —	2.80	3.60	v
VEC(Note 2)		$IE = 1200A, VGE = 0V, Tj = 125^{\circ}C$ (Not	e 4) —	2.70	—	
td(on)	Turn-on delay time	VCC = 1650V, IC = 1200A, VGE = ±15V	_	—	1.60	μs
tr	Turn-on rise time	$RG(on) = 1.6\Omega, Tj = 125^{\circ}C, Ls = 100nH$	_	—	1.00	μs
Eon	Turn-on switching energy	Inductive load	_	1.60	—	J/pulse
td(off)	Turn-off delay time	VCC = 1650V, IC = 1200A, VGE = ±15V	_	—	2.50	μs
tr	Turn-off fall time	$RG(off) = 1.6\Omega, Tj = 125^{\circ}C, Ls = 100nH$	_	—	1.00	μs
Eoff	Turn-off switching energy	Inductive load		1.55	—	J/pulse
trr (Note 2)	Reverse recovery time	VCC = 1650V, IC = 1200A, VGE = ±15V	_	—	1.40	μs
Qrr (Note 2)	Reverse recovery charge	$RG(on) = 1.6\Omega$, Tj = 125°C, Ls = 100nH		800	_	μC
Erec (Note 2)	Reverse recovery energy	Inductive load	_	0.90	_	J/pulse

Note 1. Pulse width and repetition rate should be such that junction temperature (Tj) does not exceed Topmax rating (125°C). 2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWDi).

Junction temperature (Ti) should not exceed Timax rating (150°C).
 Pulse width and repetition rate should be such as to cause negligible temperature rise.



HIGH POWER SWITCHING USE INSULATED TYPE

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THERMAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min	Тур	Max	Unit
Rth(j-c)Q	Thermal resistance	Junction to Case, IGBT part	_		8.5	K/kW
Rth(j-c)R		Junction to Case, FWDi part			17.0	K/kW
Rth(c-f)	Contact thermal resistance	Case to Fin, λgrease = 1W/m·K	—	6.0	_	K/kW

MECHANICAL CHARACTERISTICS

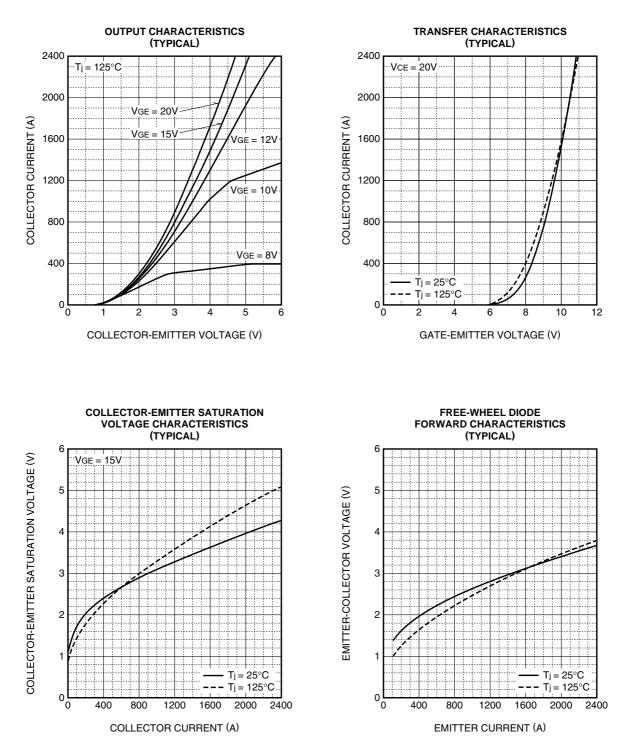
Symbol	Item	Conditions	Limits			1.1
			Min	Тур	Max	Unit
м	Mounting torque	M8 : Main terminals screw	7.0	—	13.0	N∙m
		M6 : Mounting screw	3.0	—	6.0	
		M4 : Auxiliary terminals screw	1.0	—	2.0	
_	Mass		—	1.5	—	kg
CTI	Comparative tracking index		600	—	—	—
da	Clearance distance in air		19.5	—	—	mm
ds	Creepage distance along surface		32.0	—	—	mm
LC-E(int)	Internal inductance	IGBT part	_	10	—	nH
RC-E(int)	Internal lead resistance	$TC = 25^{\circ}C$	_	0.16	_	mΩ



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HIGH POWER SWITCHING USE INSULATED TYPE

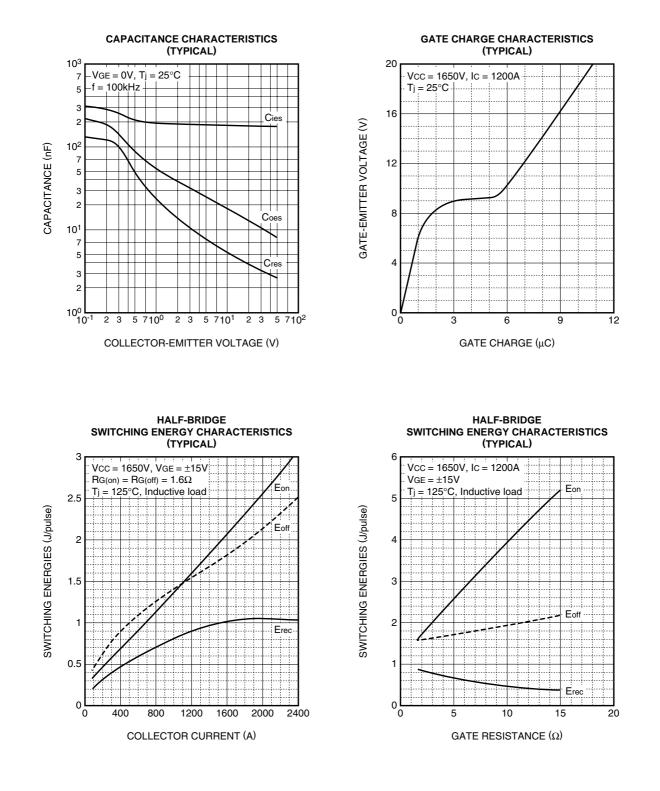
PERFORMANCE CURVES





HIGH POWER SWITCHING USE INSULATED TYPE

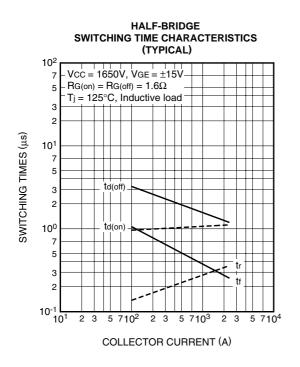
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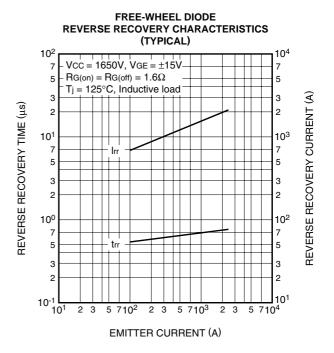


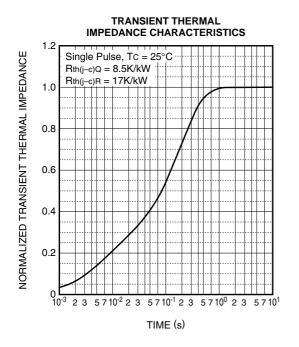


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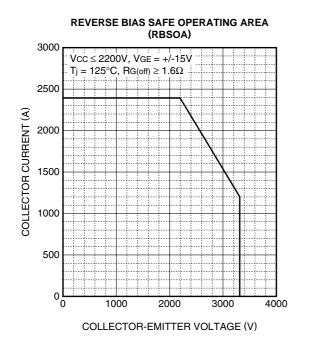


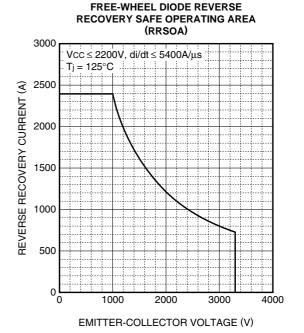




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