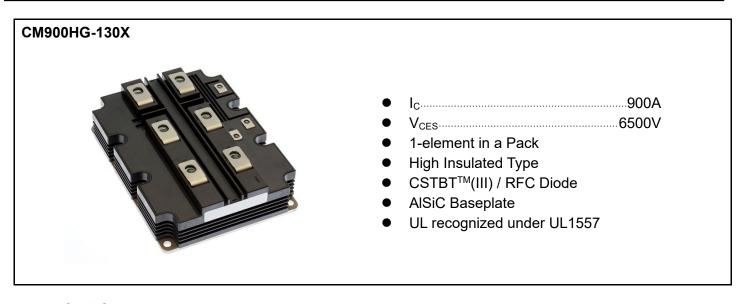


< High Voltage Insulated Gate Bipolar Transistor: HVIGBT >

### CM900HG-130X

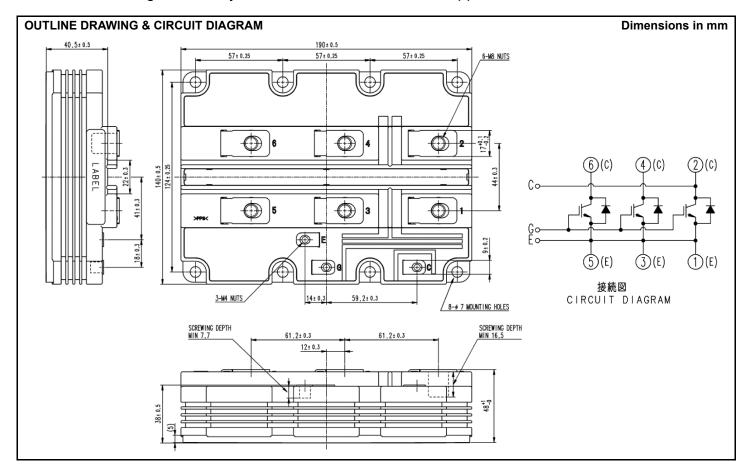
HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules



#### **APPLICATION**

Traction drives, High Reliability Converters / Inverters, DC choppers



#### < High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

### CM900HG-130X

HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **MAXIMUM RATINGS**

Symbol	Item	Conditions	Ratings	Unit
		$V_{GE} = 0V, T_j = 150^{\circ}C$	6500	
V <sub>CES</sub>	Collector-emitter voltage	$V_{GE} = 0V, T_j = 25^{\circ}C$	6300	V
		$V_{GE} = 0V, T_j = -50^{\circ}C$	5700	
$V_{GES}$	Gate-emitter voltage	$V_{CE} = 0V, T_j = 25^{\circ}C$	± 20	V
Ic	Callantan assumant	DC, T <sub>c</sub> = 115°C	900	Α
I <sub>CRM</sub>	Collector current	Pulse (Note 1)	1800	Α
I <sub>E</sub>	Cmitter current (41 + 6)	DC, T <sub>c</sub> = 95°C	900	Α
I <sub>ERM</sub>	Emitter current (Note 2)	Pulse (Note 1)	1800	Α
P <sub>tot</sub>	Maximum power dissipation (Note 3)	T <sub>c</sub> = 25°C, IGBT part	12500	W
V <sub>iso</sub>	Isolation voltage	RMS, sinusoidal, f = 60Hz, t = 1 min.	10200	V
$Q_{PD}$	Partial discharge	V1 = 6900 Vrms, V2 = 5100 Vrms, 60 Hz	10	рС
Tj	Junction temperature		−50 ~ +150	°C
T <sub>jop</sub>	Operating junction temperature		−50 ~ +150	°C
T <sub>stg</sub>	Storage temperature		<b>−55 ~ +150</b>	°C
t <sub>psc</sub>	Short circuit pulse width	$V_{CC} = 4500V, V_{CE} \le V_{CES}, V_{GE} = 15V, T_j = 150^{\circ}C$	10	μs

#### **ELECTRICAL CHARACTERISTICS**

Symbol	Item	Conditions			Limits		Unit
Symbol	item	Conditions		Min	Тур	Max	Unit
			T <sub>i</sub> = 25°C	_	_	6.0	
I <sub>CES</sub>	Collector cutoff current	$V_{CE} = V_{CES}, V_{GE} = 0V$	T <sub>i</sub> = 125°C	_	5.0	_	mA
			T <sub>i</sub> = 150°C	_		150.0	
$V_{GE(th)}$	Gate-emitter threshold voltage	$V_{CE} = 10V, I_{C} = 90mA, T_{j} = 25^{\circ}C$		6.5	7.0	7.5	V
I <sub>GES</sub>	Gate leakage current	$V_{GE} = V_{GES}$ , $V_{CE} = 0V$ , $T_j = 25$ °C		-0.5		0.5	μA
Cies	Input capacitance	\\ -40\\\\ -0\\\ f-400\\		_	151	_	nF
Coes	Output capacitance	$V_{CE} = 10V, V_{GE} = 0V, f = 100kHz$		_	6.3	_	nF
$C_{res}$	Reverse transfer capacitance	T <sub>j</sub> = 25°C		_	8.0	_	nF
$Q_{G}$	Total gate charge	$V_{CC}$ = 3600V, $I_{C}$ = 900A, $V_{GE}$ = ±15V		_	9.9	_	μC
		I <sub>C</sub> = 900A (Note 4)	T <sub>j</sub> = 25°C	_	2.50	_	
$V_{CEsat}$	Collector-emitter saturation voltage		T <sub>j</sub> = 125°C	_	3.20	_	V
		V <sub>GE</sub> = 15V	T <sub>j</sub> = 150°C	_	3.30	3.80	
t <sub>d(on)</sub>	Turn-on delay time		T <sub>j</sub> = 150°C	_	_	1.45	μs
t <sub>r</sub>	Rise time	V <sub>CC</sub> = 3600V	T <sub>j</sub> = 150°C	_		0.50	μs
	Turn-on switching energy (per pulse) (Note 5)	I <sub>C</sub> = 900A	T <sub>j</sub> = 25°C	_	6.10	_	J
E <sub>on(10%)</sub>		$\begin{aligned} R_{G(on)} &= 4.3\Omega & T_j \\ L_s &= 150 \text{nH} & T_j \end{aligned}$	T <sub>j</sub> = 125°C	_	6.60	_	
			T <sub>j</sub> = 150°C	_	7.50	_	
	Turn-on switching energy (per pulse) (Note 6)		T <sub>j</sub> = 25°C	_	6.30	_	J
E <sub>on</sub>			T <sub>j</sub> = 125°C	_	7.00	_	
			T <sub>j</sub> = 150°C	_	7.90	_	
			T <sub>j</sub> = 25°C	_	5.90	_	
$t_{d(off)}$	Turn-off delay time		T <sub>j</sub> = 125°C	_	7.00	_	μs
			$T_{j} = 150^{\circ}C$	_	7.00	10.5	
		V <sub>CC</sub> = 3600V	$T_j = 25^{\circ}C$	_	0.50	_	
t <sub>f</sub>	Fall time	I <sub>C</sub> = 900A	T <sub>j</sub> = 125°C	_	1.00	_	μs
		$V_{GE} = \pm 15V$	T <sub>j</sub> = 150°C	_	1.00	1.50	l
E <sub>off(10%)</sub>	Turn-off switching energy	$R_{G(off)} = 30\Omega$	T <sub>j</sub> = 25°C	_	3.60		
		L <sub>s</sub> = 150nH	T <sub>j</sub> = 125°C	_	5.80	_	J
	(per pulse) (Note 5)	Inductive load $T_j = 15$		_	6.00	_	<u></u>
	Turns off quitables a second	$T_j = 25^{\circ}$ C $T_j = 125^{\circ}$		_	3.70	_	
E <sub>off</sub>	Turn-off switching energy			_	6.00	_	J
	(per pulse) (Note 6)		T <sub>i</sub> = 150°C		6.20	_	

#### < High Voltage Insulated Gate Bipolar Transistor : HVIGBT >

#### CM900HG-130X

HIGH POWER SWITCHING USE

INSULATED TYPE 5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

#### **ELECTRICAL CHARACTERISTICS**

Symbol Item		Conditions		Limits			Unit	
Symbol	item		Conditions		Min	Тур	Max	Unit
			1 000 A (Note 4)	T <sub>j</sub> = 25°C	_	2.50	_	
V <sub>EC</sub>	Emitter-collector voltage	(Note 2)	I <sub>E</sub> = 900A (Note 4)	T <sub>j</sub> = 125°C	_	3.20	_	V
			$V_{GE} = 0V$	T <sub>j</sub> = 150°C	_	3.30	3.80	
				T <sub>j</sub> = 25°C	1	2.00	_	
t <sub>rr</sub>	Reverse recovery time	(Note 2)		T <sub>j</sub> = 125°C	_	2.40	_	μs
				T <sub>j</sub> = 150°C	_	2.50	_	
				T <sub>j</sub> = 25°C	1	1250	_	
Irr	Reverse recovery current	(Note 2)		T <sub>i</sub> = 125°C	-	1200	_	Α
			T <sub>i</sub> = 150°C	-	1200	_		
			V <sub>CC</sub> = 3600V	T <sub>j</sub> = 25°C	-	1800	_	
Q <sub>rr(10%)</sub>	Reverse recovery charge	(Note 2,7)	I <sub>C</sub> = 900A	T <sub>j</sub> = 125°C	_	2300	_	μC
		$V_{GE} = \pm 15V$	T <sub>i</sub> = 150°C	-	2400	_		
			$R_{G(on)} = 4.3\Omega$	T <sub>j</sub> = 25°C	_	1850	_	
Q <sub>rr</sub>	Reverse recovery charge	(Note 2, 6)	$L_s = 150 nH$	T <sub>j</sub> = 125°C	_	2350	_	μC
			Inductive load	T <sub>j</sub> = 150°C	-	2500	_	
	Poverse receivery energy			T <sub>j</sub> = 25°C	_	2.90		
E <sub>rec(10%)</sub>	Reverse recovery energy	(Note 2, 5)		T <sub>j</sub> = 125°C	_	4.20	_	J
	(per pulse)	, , ,		T <sub>j</sub> = 150°C	-	4.50	_	
	Reverse recovery energy			T <sub>j</sub> = 25°C	_	3.00	_	
E <sub>rec</sub>		(Note 2, 6)		T <sub>j</sub> = 125°C	_	4.30	_	J
	(per pulse)	, -,		T <sub>j</sub> = 150°C	_	4.80	_	

#### THERMAL CHARACTERISTICS

Cumbal	Item	Conditions		Limits		
Symbol				Тур	Max	Unit
R <sub>th(j-c)Q</sub>	Thermal registeres	Junction to Case, IGBT part		_	10.0	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to Case, FWDi part		_	16.0	K/kW
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, $\lambda_{grease}$ = 1W/m K, $D_{(c-s)}$ = 80 $\mu$ m	_	5.0	_	K/kW

#### **MECHANICAL CHARACTERISTICS**

Comple el	Item	O and distance	Limits			Limit
Symbol		Conditions	Min	Тур	Max	Unit
Mt		M8 : Main terminals screw	7.0	1	19.0	N⋅m
Ms	Mounting torque	M6 : Mounting screw	3.0	1	6.0	N⋅m
$M_t$		M4 : Auxiliary terminals screw	1.0	1	3.0	N⋅m
m	Mass		_	1.5	_	kg
CTI	Comparative tracking index		600	1	-	_
da	Clearance		26.0	1	-	mm
ds	Creepage distance		56.0	1	1	mm
L <sub>P CE</sub>	Parasitic stray inductance			13.5	1	nΗ
R <sub>CC'+EE'</sub>	Internal lead resistance	T <sub>C</sub> = 25°C	_	0.12	_	mΩ

Note1. Pulse width and repetition rate should be such that junction temperature (Tj) does not exceed Tjopmax rating.

Note2. The symbols represent characteristics of the anti-parallel, emitter to collector free-wheel diode (FWD<sub>i</sub>).

Note3. Junction temperature  $(T_j)$  should not exceed  $T_{jmax}$  rating (150°C).

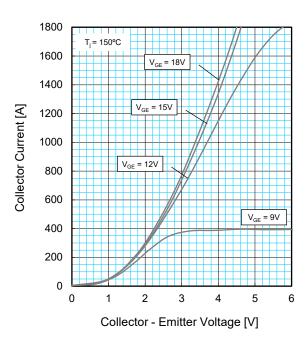
Note4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

Note5. The integration range of switching energies is from 10%VcE to 10%Ic(10%IE).

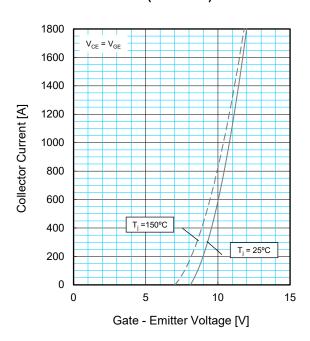
Note6. Definition of all items is according to IEC 60747, unless otherwise specified.

Note7. The integration range of reverse recovery charge is from  $I_E = 0A$  to  $10\%I_E$ .

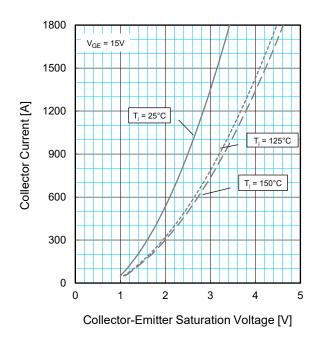
# OUTPUT CHARACTERISTICS (TYPICAL)



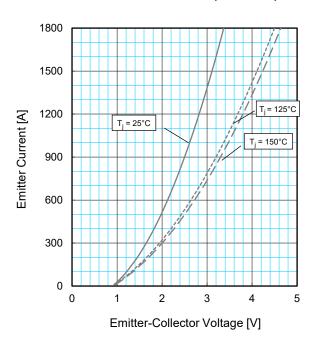
### TRANSFER CHARACTERISTICS (TYPICAL)



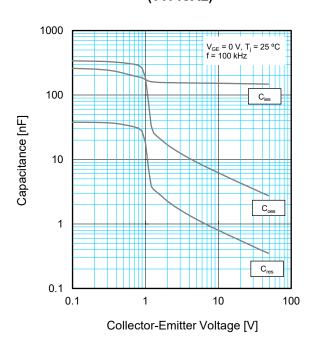
# COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



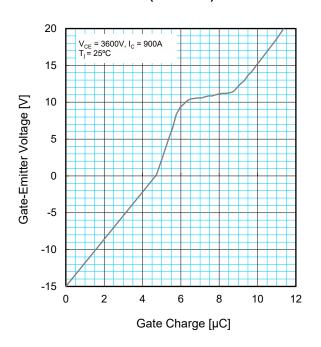
# FREE-WHEEL DIODE FORWARD CHARACTERISTICS (TYPICAL)



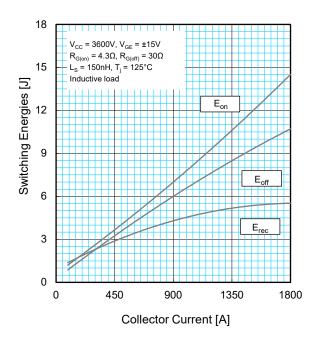
# CAPACITANCE CHARACTERISTICS (TYPICAL)



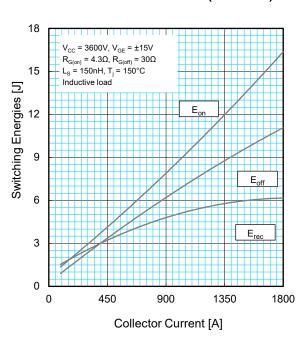
### GATE CHARGE CHARACTERISTICS (TYPICAL)



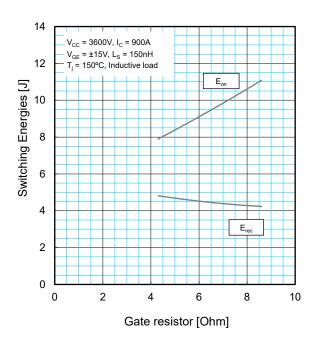
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



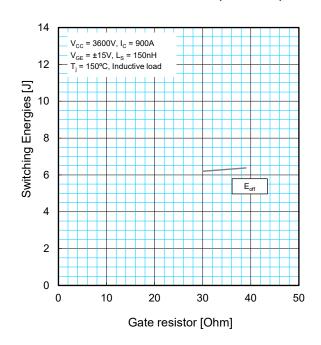
# HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



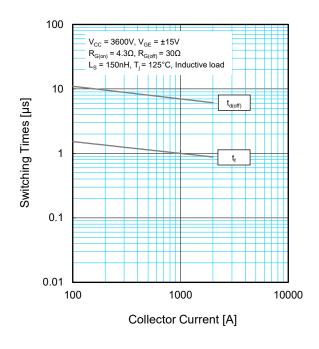
### HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



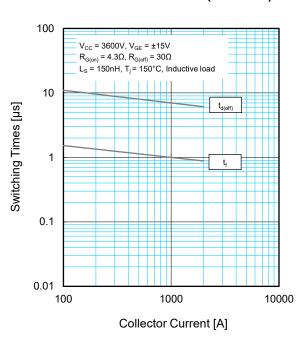
#### HALF-BRIDGE SWITCHING ENERGY CHARACTERISTICS (TYPICAL)



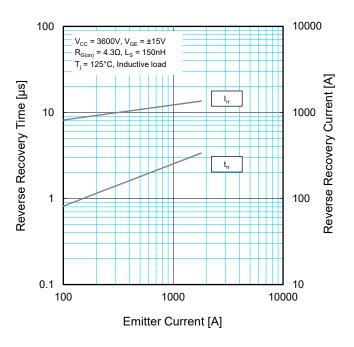
# HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



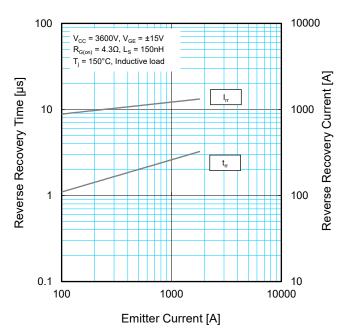
# HALF-BRIDGE SWITCHING TIME CHARACTERISTICS (TYPICAL)



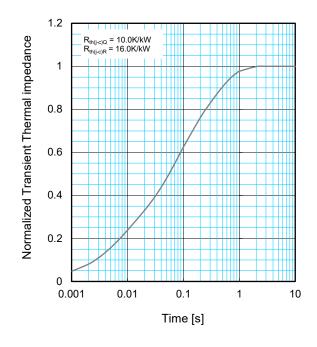
# FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



### FREE-WHEEL DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



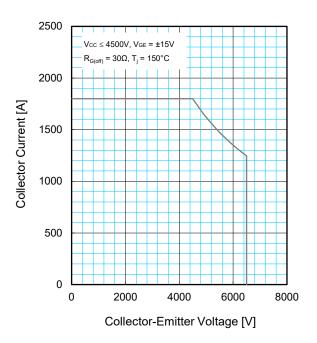
### TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS



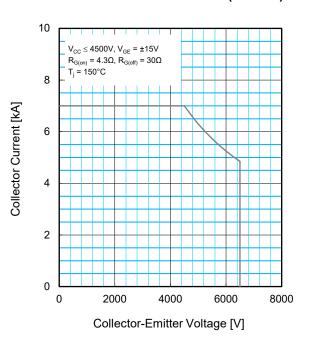
$$Z_{th(j-c)}(t) = \sum_{i=1}^{n} R_{i} \left\{ 1 - \exp^{\left(-\frac{t}{\tau_{i}}\right)} \right\}$$

	1	2	3	4
R <sub>i</sub> / R <sub>th(j-c)</sub> :	0.0096	0.1893	0.4044	0.3967
τ <sub>i</sub> [sec] :	0.0001	0.0058	0.0602	0.3512

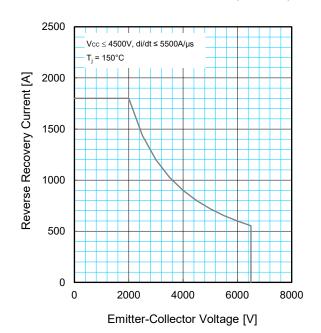
# REVERSE BIAS SAFE OPERATING AREA (RBSOA)



### SHORT CIRCUIT SAFE OPERATING AREA (SCSOA)



# FREE-WHEEL DIODE REVERSE RECOVERY SAFE OPERATING AREA (RRSOA)



CM900HG-130X HIGH POWER SWITCHING USE INSULATED TYPE

5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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5th-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

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